

ABSTRACT

The present invention provides an amplifying circuit capable of accomplishing high-impedance input/output, and providing a high gain in low power consumption. The amplifier 208 amplifies a signal received through an input terminal IN, and outputs the signal through an output terminal OUT. A control circuit comprised of the inductors 203 and 204, and the switches 209 and 210 turns input/output impedances of the amplifier 208 into a high impedance.